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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Applicant / Conf. No.	Unknown /
				Filing Date	January 16, 2004
				First Named Inventor	Stephen M. Trimberger
				Art Unit	Unknown
				Examiner Name	Unknown
Sheet	2	of	2	Attorney Docket Number	X-1460 US

OTHER – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		XILINX, INC.; "Virtex Series Configuration Architecture User Guide," XAPP151, September 27, 2000, pages 1-45, v1.5, available from Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124.	
		K. BRANDENBURG et al.; "An introduction to MPEG Layer-3," EBU Technical Review, June 2000, Pages 1-15, available from EBU Technical Review, http://www.ebu.ch/trev_283-popp.pdf .	
		ZHIYUAN LI et al.; "Configuration Compression for Virtex FPGAs," IEEE Symposium on FPGAs for Custom Computing Machines, June 2001, pages 1-15, available from IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997.	

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